

Leakage

DESCRIPTION:

MAIN FEATURES

Built In Half Wave Rectification And Power Supply Voltage Stabilizing Module

Low Offset Amplifier

Bidirectional Threshold Comparator

Under voltage Protection

SOT23-6 Package

Suitable For Various Voltage Systems

Anti Interference Delay

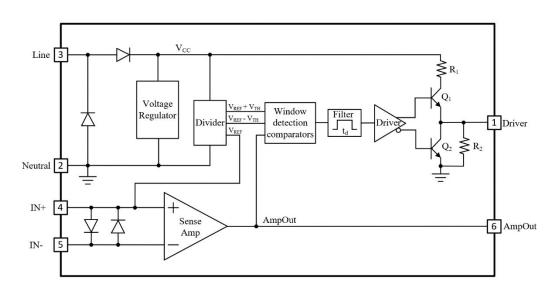
Adjustable Detection Sensitivity

Meets UL 943B Requirements

Application

→ RCD

Block Diagram





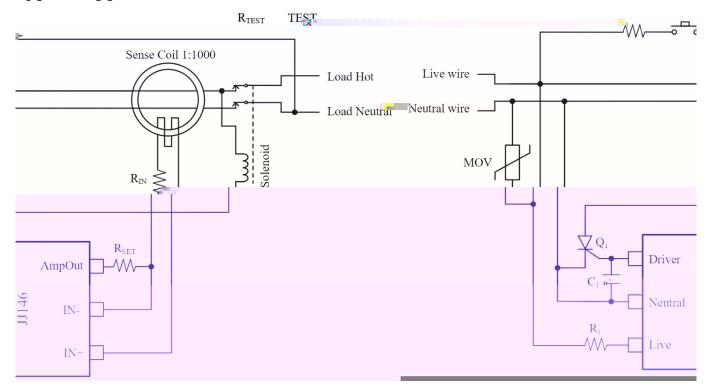


ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Test Conditions	Min	Тур	Max	Units



Typical Applications



Load Hot

Neutral wire



Functional Description

RCD



Calculation of RSET Resistor

$$R_{SE} = \frac{-\frac{P}{P}}{I_{FA L}}$$

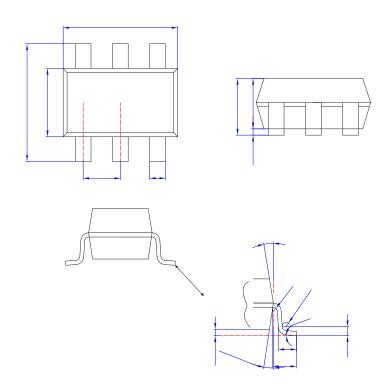
$$R_{SE} = \frac{-\frac{H}{P}}{I_{FA L}}$$

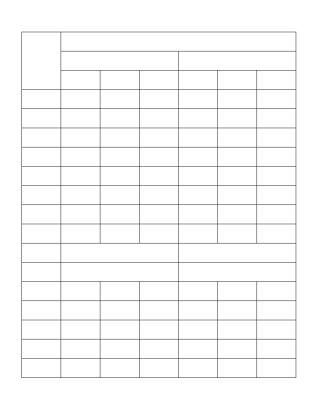
Calculation of VOS Trip Threshold Error



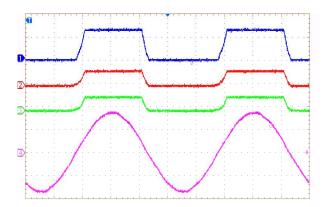
ORDERING INFORMATION

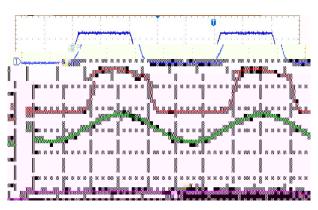
Package dimensions

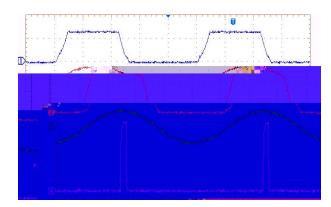


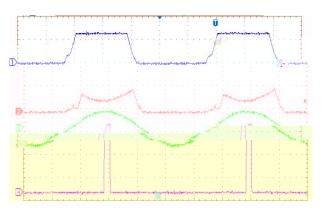






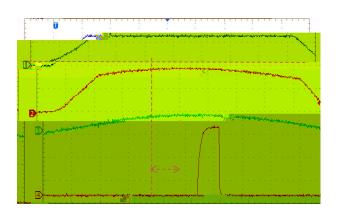


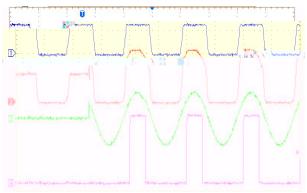




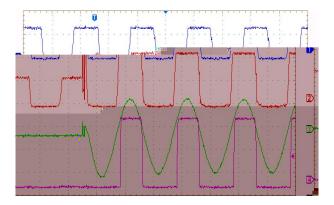


15K Ground Fault

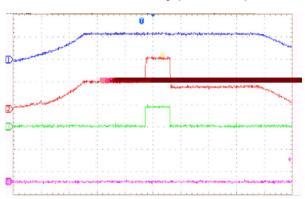




500 Ground Fault



Internal Filter Delay (Fault Time<1ms)



CHT. VLIVE (13W7771V)

CH2: Ampour (3 v/prv)

CH3: I_{FAULT} (20mA/DIV)

CH4: Driver (1V/DIV)

Time: 1ms/DIV

Fault time is slightly less than 1ms,

Driver not triggerea.

